

PATENT

Docket No.: **122988-05007303**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : ATTN: Certificate of Correction Branch
Inventor(s): Chang-Soo KIM et al. : Conf. No.: 1143
U.S. Patent No. 7,197,598 : Group Art Unit: 2188
Issued Date: March 27, 2007 : Examiner: Duc T Doan
: :

For: APPARATUS AND METHOD FOR FILE-LEVEL STRIPING

REQUEST FOR CERTIFICATE OF CORRECTION OF
OFFICE MISTAKE UNDER 37 CFR 1.322

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In reviewing the above-identified patent, an error on the part of the Patent and Trademark Office was discovered. Correction is required in order to conform with the Patent to the Official Record in the application.

The error noted is set forth on the two attached copies of form PTO-1050 Rev. 2-93 in the manner required by the Commissioner's Notice.

The change requested herein corrects an error of the Patent and Trademark Office and the Certificate should be issued without expense to the patentee under Rule 1.322 of the Rules of Practice. Accordingly, issuance of the Certificate of Correction is requested.

Please charge any shortage in fees due in connection with the filing of this paper to
Deposit Account 07-1337 and please credit any excess fees to such deposit account.

Respectfully submitted,

LOWE HAUPTMAN HAM & BERNER, LLP

/Yoon S Ham/
Yoon S. Ham
Registration No. 45,307

1700 Diagonal Road, Suite 300
Alexandria, Virginia 22314
Telephone: 703-684-1111
Facsimile: 703-518-5499
Date: November 8, 2007

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CERTIFICATE OF CORRECTION**Page 1 of 1

PATENT NO. : 7,197,598

APPLICATION NO.: 10/621,461

ISSUE DATE : March 27, 2007

INVENTOR(S) : Chang-Soo KIM et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

5. The file level striping method of claim [[4]] 1, wherein said step of allocation a physical block and said step of modifying the last disk ID value further comprising the steps of: requesting a file I/O operation; determining which logical block corresponds to the requested file 1/0 operation; requesting the volume manager or a lower level I/O system to perform said determined logical block I/O operation; performing an address mapping process in order to determine which disk and which physical block therein correspond to said logical block; performing I/O operation for the physical block determined to correspond to said logical block at the address mapping process; checking the value of the last disk ID of the mode corresponding to the file if physical block allocation turns out to be required since the logical block is used for the first time at the address mapping process; selecting the disk of a number next to the value of last disk ID if the value of the last disk ID turns out to be in the range from 0 to the number of the disks associated with the logical volume minus 1; performing physical block allocation referring to the free space bitmap of said selected disk; updating the mapping table with the allocation result and performing I/O operation for the file on the physical block; changing the value of the last disk ID if I/O operation on the physical block is completed; and performing I/O operations repeatedly on the physical block for the physical block allocation to be distributed uniformly across the whole disks.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Lowe Hauptman Ham & Berner LLP
1700 Diagonal Road Suite 300
Alexandria, VA 22314

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.